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Inventor Information for 10/774914

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US 20060083981 A1	US- PGPUB	20060420	Battery can and manufacturing method thereof and battery using the same	429/164	428/681; 428/682; 429/176	Mori; Katsuhiko et al.
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US 20060066769 A1	US- PGPUB	20060330	Electronic apparatus	349/58		Minaguchi; Hiroyuki et al.
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US 20060044889 A1	US- PGPUB	20060302	Internal voltage level control circuit and semiconductor memory device as well as method of controlling the same	365/189.11		Takahashi; Hiroyuki et al.
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US 20030062814 A1	US- PGPUB	20030403	Light source device, method of producing the same, and display apparatus	313/46		Kobayashi, Tetsuya et al.
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US 20020097209 A1	US- PGPUB	20020725	Liquid crystal display device	345/87		Takahashi, Hiroyuki et al.
US 20020096863 A1	US- PGPUB	20020725	Head protection air bag device	280/730.2		Tanase, Toshinori et al.
US 20020084835 A1	US- PGPUB	20020704	Semiconductor device with reduced leakage of current	327/544		Takahashi, Hiroyuki
US 20020080666 A1	US- PGPUB	20020627	Semiconductor memory device	365/200		Takahashi, Hiroyuki et al.
US 20020054314 A1	US- PGPUB	20020509	Color image processing method and apparatus, and storage medium	358/1.13		Takahashi, Hiroyuki
US 20020052847 A1	US- PGPUB	20020502	Information providing system	705/51	705/1	Shioda, Takehiko et al.
US 20020042334 A1	US- PGPUB	20020411	Centrifuge	494/10	494/7	Tetsu, Kenichi et al.
US 20020041524 A1	US- PGPUB	20020411	Semiconductor integrated circuit	365/189.08		Takahashi, Hiroyuki et al.
US 20020040987 A1	US- PGPUB	20020411	Semiconductor device	257/262	257/E27.06	Takahashi, Hiroyuki

US 20020038948 A1	US- PGPUB	20020404	GAS GENERATING DEVICE FOR AIR BAG AND AIR BAG INFLATING METHOD	280/736	280/741	YAMAMORI, KIYOSHI et al.
US 20020027198 A1	US- PGPUB	20020307	Method and apparatus for charged particle beam exposure	250/307		Nagata, Koji et al.
US 20020024864 A1	US- PGPUB	20020228	SENSE AMPLIFIER CIRCUIT AND SEMICONDUCTOR STORAGE DEVICE	365/207		Sudo, Naoaki et al.
US 20020024362 A1	US- PGPUB	20020228	Logic circuit with single charge pulling out transistor and semiconductor integrated circuit using the same	326/110		Takahashi, Hiroyuki et al.
US 20020024203 A1	US- PGPUB	20020228	Head-protection airbag device	280/730.2		Takahashi, Hiroyuki
US 20020021839 A1	US- PGPUB	20020221	Image processing method, relative density detecting method and image processing apparatus	382/194		Takahashi, Hiroyasu
US 20020021159 A1	US- PGPUB	20020221	Delay circuit and method	327/283		Takahashi, Hiroyuki
US 20020011863 A1	US- PGPUB	20020131	IC chip tester with heating element for preventing condensation	324/760		Takahashi, Hiroyuki et al.
US 20020011726 A1	US- PGPUB	20020131	Seat belt guide	280/801.1		Izume, Tomoharu et al.
US 20020004267 A1	US- PGPUB	20020110	Sense amplifier circuit and semiconductor storage device	438/201		Sudo, Naoaki et al.
US 20010055430 A1	US- PGPUB	20011227	APPARATUS AND METHOD FOR PROCESSING IMAGE	382/284		TAKAHASHI, HIROYUKI et al.
US 20010054886 A1	US- PGPUB	20011227	Input circuit and output circuit	323/282		Takahashi, Hiroyuki et al.
US 20010050732 A1	US- PGPUB	20011213	Liquid crystal display device having small picture frame region	349/58		Okamoto, Kazuhiro et al.
US 20010040469	US- PGPUB	20011115	LOGIC CIRCUIT WITH SINGLE	326/110		TAKAHASHI, HIROYUKI et

A1			CHARGE PULLING OUT TRANSISTOR AND SEMICONDUCTOR INTEGRATED CIRCUIT USING THE SAME			al.
US 20010039389 A1	US- PGPUB	20011108	Ultrasonic operation apparatus for performing follow-up control of resonance frequency drive of ultrasonic oscillator by digital PLL system using DDS (direct digital synthesizer)	601/2		Sakurai, Tomohisa et al.
US 20010032935 A1	US- PGPUB	20011025	Multi-grid type microstrip gas chamber	250/385.1	250/374	Takahashi, Hiroyuki
US 20010029315 A1	US- PGPUB	20011011	Surgical operation system	600/101	606/34	Sakurai, Tomohisa et al.
US 20010024382 A1	US- PGPUB	20010927	Semiconductor memory device	365/189.01	365/154; 365/189.05; 365/189.12; 365/193	Shimoyama, Takato et al.
US 20010015659 A1	US- PGPUB	20010823	Logic circuit	326/112		Sato, Fumihiko et al.
US 20010005246 A1	US- PGPUB	20010628	LIQUID CRYSTAL DISPLAY DEVICE	349/117	349/123; 349/175; 349/181	TAKIGUCHI, YASUYUKI et al.
US 20010005148 A1	US- PGPUB	20010628	Semiconductor intergrated circuit having logic circuit comprising transistors with lower threshold voltage values and improved pattern layout	326/112		Takano, Susumu et al.
US 7042266 B2	USPAT	20060509	Delay circuit and method	327/288	327/284	Takahashi; Hiroyuki
US 7035154 B2	USPAT	20060425	Semiconductor memory device and its test method as well as test circuit	365/201	365/222; 365/230.03	Takahashi; Hiroyuki et al.
US 7030607 B2	USPAT	20060418	Variable reluctance-type resolver	324/207.25		Ando; Katsuaki et al.
US 7029833	USPAT	20060418	Image display medium,	430/333	430/19; 430/334;	Takahashi;

B2			process for forming image, and multicolor image-forming apparatus		430/335; 430/339; 430/962	Hiroyuki et al.
US 7023771 B2	USPAT	20060404	Optical information recording apparatus	369/47.5	369/47.32; 369/53.31	Takata; Shinichi et al.
US 7023747 B2	USPAT	20060404	Semiconductor memory device and address conversion circuit	365/200	365/222; 365/230.03	Takahashi; Hiroyuki
US 7023434 B2	USPAT	20060404	Image processing apparatus and image processing program	345/419	345/474; 345/582; 345/633	Takahashi; Hiroyuki et al.
US 7015632 B2	USPAT	20060321	Light source device, method of producing the same, and display apparatus	313/42	313/46; 313/49; 313/625; 362/218; 362/264; 362/373	Kobayashi; Tetsuya et al.
US 7006401 B2	USPAT	20060228	Semiconductor storage device and refresh control method thereof	365/222	365/189.04	Takahashi; Hiroyuki et al.
US 7005112 B1	USPAT	20060228	Thermal decomposer for waste	422/186.21	219/69.1	Takahashi; Hiroyuki et al.
US 7002868 B2	USPAT	20060221	High-speed, two-port dynamic random access memory (DRAM) with a late-write configuration	365/222	365/149; 365/189.04; 365/189.05; 365/230.05; 365/230.08	Takahashi; Hiroyuki
US 7002854 B2	USPAT	20060221	Internal voltage level control circuit and semiconductor memory device as well as method of controlling the same	365/189.09	365/226	Takahashi; Hiroyuki et al.
US 6998329 B2	USPAT	20060214	SOI wafer producing method, and wafer separating jig	438/458	414/935	Aga; Hiroji et al.
US 6992385 B2	USPAT	20060131	Semiconductor device, a method of manufacturing the same and an electronic device	257/735	257/666; 257/667	Satou; Yukihiro et al.
US 6985629 B2	USPAT	20060110	Image processing method, relative density detecting method and image processing apparatus	382/225	358/2.1; 382/307	Takahashi; Hiroyasu
US 6985245 B1	USPAT	20060110	Image processing apparatus and system and control method therefor, image data	358/1.15	358/1.11; 358/1.9	Takahashi; Hiroyuki

			processing method, image forming apparatus and control method therefor, controller therefor, and storage medium storing the control method for processing image data having different printing attributes			
US 6977859 B2	USPAT	20051220	Semiconductor memory device and control method thereof	365/222	365/233	Takahashi; Hiroyuki
US 6973522 B1	USPAT	20051206	Microcomputer with program-revision ability	710/269	712/220; 714/726; 714/8	Takahashi; Hiroyuki
US 6960001 B2	USPAT	20051101	Backlight apparatus, and a liquid crystal display (LCD) therewith	362/218	362/225; 362/260; 362/373	Nitto; Eiji et al.
US 6956548 B2	USPAT	20051018	Display device	345/87	349/149	Yarita; Katsuhiko et al.
US 6953424 B2	USPAT	20051011	Rotor driving apparatus with temperature adjustment of elastic supporting portion	494/10	494/13; 494/14; 494/16; 494/82	Kusumoto; Shoji et al.
US 6947345 B2	USPAT	20050920	Semiconductor memory device	365/222	365/229	Takahashi; Hiroyuki et al.
US 6944081 B2	USPAT	20050913	Semiconductor storage and its refreshing method	365/222	365/230.03; 365/230.06	Takahashi; Hiroyuki et al.
US 6938715 B2	USPAT	20050906	Hood structure of motor vehicle	180/274	180/69.21; 280/730.1	Hamada; Makoto et al.
US 6933750 B2	USPAT	20050823	Buffer circuit, buffer tree, and semiconductor device	326/82	326/83; 326/86	Takahashi; Hiroyuki et al.
US 6933506 B2	USPAT	20050823	Microstrip gas chamber	250/385.1	250/336.1; 250/374	Takahashi; Hiroyuki
US 6930945 B2	USPAT	20050816	Semiconductor memory device and control method thereof	365/222	365/233	Takahashi; Hiroyuki
US 6929575 B2	USPAT	20050816	Pulley	474/161	474/152	Takahashi; Hiroyuki et al.
US 6928020 B2	USPAT	20050809	Semiconductor memory device	365/222	365/189.01; 365/230.01; 365/230.08	Takahashi; Hiroyuki et al.
US 6925016 B2	USPAT	20050802	Semiconductor memory and method for entering its operation mode	365/189.07	365/189.05; 365/189.12; 365/230.06;	Takahashi; Hiroyuki et al.

					365/230.08	
US 6922371 B2	USPAT	20050726	Semiconductor storage device	365/227	365/222; 365/229	Takahashi; Hiroyuki et al.
US 6919734 B2	USPAT	20050719	Cooling fin connected to a cooling unit and a pusher of the testing apparatus	324/760	324/754	Saito; Noboru et al.
US 6913282 B2	USPAT	20050705	Head-protection airbag device	280/730.2	280/742	Takahashi; Hiroyuki
US 6913281 B2	USPAT	20050705	Head-protection airbag device	280/730.2	280/740; 280/742	Takahashi; Hiroyuki
US 6912059 B1	USPAT	20050628	Image formation system	358/1.15	358/515	Takahashi; Hiroyuki
US 6903717 B2	USPAT	20050607	Display device having driving circuit	345/100	345/211; 349/149	Takahashi; Hiroyuki et al.
US 6898142 B2	USPAT	20050524	Semiconductor memory, method for controlling refreshment of it, and method for setting memory cell array specific area for realizing the control method	365/222	365/229; 365/230.06	Takahashi; Hiroyuki
US 6896953 B2	USPAT	20050524	Wiring board and process of producing the same	428/210	174/258; 257/E23.009; 257/E23.062	Taga; Shigeru et al.
US 6879537 B2	USPAT	20050412	Semiconductor storage device having a plurality of operation modes	365/222	365/189.09; 365/233; 365/236	Takahashi; Hiroyuki et al.
US 6876592 B2	USPAT	20050405	Semiconductor memory device	365/222	365/195; 365/233.5	Takahashi; Hiroyuki et al.
US 6864991 B1	USPAT	20050308	Information processing apparatus for displaying data related to image forming apparatus, and information processing method therefor	358/1.15	710/19; 710/20	Takahashi; Hiroyuki
US 6856566 B2	USPAT	20050215	Timer circuit and semiconductor memory incorporating the timer circuit	365/222	365/211; 365/233	Takahashi; Hiroyuki et al.
US 6850449 B2	USPAT	20050201	Semiconductor memory device having mode storing one bit data in two memory cells and method of controlling same	365/222	365/194; 365/205	Takahashi; Hiroyuki
US 6834020	USPAT	20041221	Semiconductor storage	365/222	365/189.01;	Takahashi;

B2			device		365/230.01; 365/230.08	Hiroyuki et al.
US 6834004 B2	USPAT	20041221	Semiconductor integrated circuit having logic circuit comprising transistors with lower threshold voltage values and improved pattern layout	365/51	326/105; 326/87; 365/63	Takano; Susumu et al.
US 6831484 B2	USPAT	20041214	Semiconductor integrated circuit having logic circuit comprising transistors with lower threshold voltage values and improved pattern layout	326/121	326/119	Takano; Susumu et al.
US 6827170 B2	USPAT	20041207	Hood airbag assembly	180/274		Hamada; Makoto et al.
US 6820893 B2	USPAT	20041123	Head protection air bag device	280/730.2	280/743.1	Tanase; Toshinori et al.
US 6813212 B2	USPAT	20041102	Semiconductor memory device and refresh control circuit	365/222	365/233.5	Takahashi; Hiroyuki et al.
US 6809989 B2	USPAT	20041026	Semiconductor storage device	365/230.08	365/233; 365/233.5	Takahashi; Hiroyuki et al.
US RE38602 E	USPAT	20040928	Optical recording medium and method for setting power of laser beam	369/47.53	369/116	Takahashi; Hiroyuki
US 6798895 B1	USPAT	20040928	Character string extraction and image processing methods and apparatus	382/101	382/177; 382/180; 382/190	Takahashi; Hiroyasu
US 6789919 B2	USPAT	20040914	Circular fluorescent lamp unit and lighting apparatus	362/216	313/573; 313/634	Imanishi; Otokazu et al.
US 6768370 B2	USPAT	20040727	Internal voltage step-down circuit	327/540		Takahashi; Hiroyuki et al.
US 6767513 B1	USPAT	20040727	Method of treating exhaust gases containing halogen based compound	422/122	422/4; 423/240S; 423/241; 95/131; 95/132	Omi; Tadahiro et al.
US 6766111 B2	USPAT	20040720	Camera system having a communication system between a camera body and a photographing lens	396/91	396/236; 396/532	Uenaka; Yukio et al.
US 6764575	USPAT	20040720	Magnetron plasma	156/345.46	118/723E;	Yamasaki;

B1			processing apparatus		118/723MA; 118/723MR; 118/723R; 156/345.42; 156/345.49; 204/298.16; 204/298.37	Tomomi et al.
US 6764437 B2	USPAT	20040720	Centrifuge with rotor having identification elements arranged along the circumference of a circle whose center coincides with the rotor's axis of rotation	494/10	494/7	Tetsu; Kenichi et al.
US 6761698 B2	USPAT	20040713	Ultrasonic operation system	601/2	600/439; 604/22; 606/1; 606/169	Shibata; Norikiyo et al.
US 6761690 B2	USPAT	20040713	Ultrasonic operation apparatus for performing follow-up control of resonance frequency drive of ultrasonic oscillator by digital PLL system using DDS (direct digital synthesizer)	600/447	600/437; 600/443; 600/459	Sakurai; Tomohisa et al.
US 6759666 B2	USPAT	20040706	Method and apparatus for charged particle beam exposure	250/492.22	250/492.2; 250/492.23	Nagata; Koji et al.
US 6751144 B2	USPAT	20040615	Semiconductor storage and method for testing the same	365/222	365/229; 365/233.5	Takahashi; Hiroyuki et al.
US 6749216 B2	USPAT	20040615	Head protecting air bag device	280/730.2	280/740; 280/742	Tanase; Toshinori et al.
US 6747622 B2	USPAT	20040608	Liquid crystal display device	345/87	345/100; 345/205; 345/206; 345/207; 345/208; 345/209; 345/211; 345/212; 345/88; 345/89; 345/92; 345/94; 345/96; 345/98	Takahashi; Hiroyuki et al.
US 6741808 B2	USPAT	20040525	Camera system having a communication system between a camera body and a photographing lens	396/303	396/532	Uenaka; Yukio et al.
US 6728158 B2	USPAT	20040427	Semiconductor memory device	365/225.7	326/10; 327/525; 327/526; 365/201; 365/205	Takahashi; Hiroyuki et al.

US 6727999 B1	USPAT	20040427	Image formation system	358/1.15	270/1.02; 270/52.02; 358/1.16; 399/1; 399/403	Takahashi; Hiroyuki
US 6724664 B2	USPAT	20040420	Low-amplitude driver circuit	365/189.05	327/108; 327/112; 365/189.09; 365/189.11	Takahashi; Hiroyuki
US 6714479 B2	USPAT	20040330	Semiconductor memory and control method	365/233.5	365/206; 365/222	Takahashi; Hiroyuki et al.
US 6707992 B2	USPAT	20040316	Camera system having a communication system between a camera body and a photographing lens	396/71	396/529	Uenaka; Yukio et al.
US 6705640 B2	USPAT	20040316	Head-protection airbag device	280/730.2	280/743.1	Takahashi; Hiroyuki
US 6703619 B2	USPAT	20040309	Multi-grid type microstrip gas chamber	250/385.1	250/374	Takahashi; Hiroyuki
US 6700412 B2	USPAT	20040302	Semiconductor device	326/119	326/112; 326/121	Takahashi; Hiroyuki
US 6697167 B1	USPAT	20040224	Image processing apparatus and method	358/1.9	358/518	Takahashi; Hiroyuki
US 6687166 B1	USPAT	20040203	Bus interface circuit and receiver circuit	365/189.05	326/26; 326/27; 365/203; 365/207; 365/208; 365/227	Takahashi; Hiroyuki et al.
US 6683967 B1	USPAT	20040127	Area extraction method, address area extraction method, address area extraction apparatus, and image processing apparatus	382/101	382/180	Takahashi; Hiroyasu
US 6679875 B2	USPAT	20040120	Medical treatment system	606/1	606/34; 606/41	Honda; Yoshitaka et al.
US 6674965 B2	USPAT	20040106	Electric power control device for camera	396/277	396/303; 396/85	Takahashi; Hiroyuki
US 6669690 B1	USPAT	20031230	Ultrasound treatment system	606/40	606/169; 606/49; 606/51; 606/52	Okada; Mitsumasa et al.
US 6666875 B1	USPAT	20031223	Surgical apparatus permitting recharge of battery-driven surgical instrument in noncontact state	606/169	30/DIG.1	Sakurai; Tomohisa et al.
US 6666860 B1	USPAT	20031223	Electric treatment system	606/34		Takahashi; Hiroyuki
US 6663623 B1	USPAT	20031216	Electric surgical operation apparatus	606/38	606/37; 606/42	Oyama; Masahide et al.

US 6655451 B2	USPAT	20031202	Heat transfer tube for falling film type evaporator	165/179	138/38; 165/133; 165/184	Tada; Yoshio et al.
US 6654495 B1	USPAT	20031125	Method and apparatus for removing ruled lines	382/178	382/257	Katoh; Shin et al.
US 6646956 B2	USPAT	20031111	One-shot signal generating circuit	365/233.5	365/189.11; 365/233	Takahashi; Hiroyuki et al.
US 6645440 B1	USPAT	20031111	Thermal decomposition furnace for exhaust gas	422/186.04		Takahashi; Hiroyuki et al.
US 6636078 B2	USPAT	20031021	Semiconductor device having a reduced leakage current	326/121	257/E27.06; 326/112; 326/119; 326/34	Takahashi; Hiroyuki
US 6623423 B2	USPAT	20030923	Surgical operation system	600/104	606/1; 606/10; 606/167; 606/170; 606/34; 606/46	Sakurai; Tomohisa et al.
US 6616588 B2	USPAT	20030909	Centrifugal separator with rotor identification	494/10		Takahashi; Hiroyuki et al.
US 6610236 B1	USPAT	20030826	Resin molded article and method for producing the same	264/234	525/199; 525/200; 525/227; 525/245; 525/276; 525/408; 525/55	Yamaguchi; Fumihiko et al.
US 6607213 B2	USPAT	20030819	Gas generating device for air bag and air bag inflating method	280/736	280/741	Yamamori; Kiyoshi et al.
US 6603345 B2	USPAT	20030805	Semiconductor device with reduced leakage of current	327/534		Takahashi; Hiroyuki
US 6603334 B1	USPAT	20030805	Low-amplitude driver circuit	327/108	327/112	Takahashi; Hiroyuki
US 6569109 B2	USPAT	20030527	Ultrasonic operation apparatus for performing follow-up control of resonance frequency drive of ultrasonic oscillator by digital PLL system using DDS (direct digital synthesizer)	601/2	606/27; 607/2	Sakurai; Tomohisa et al.
US 6556482 B2	USPAT	20030429	Semiconductor memory device	365/189.01	365/189.04	Shimoyama; Takato et al.
US 6545892 B2	USPAT	20030408	Semiconductor integrated circuit having logic circuit comprising transistors with lower threshold voltage values and improved pattern layout	365/51	326/106; 365/230.06; 365/63	Takano; Susumu et al.
US 6522789	USPAT	20030218	Apparatus and method	382/284	358/540	Takahashi;

B2			for processing images			Hiroyuki et al.
US 6522181 B1	USPAT	20030218	Semiconductor memory apparatus which can easily attain reduction of access time	327/141	327/161	Takahashi; Hiroyuki
US 6518810 B1	USPAT	20030211	Latch circuit and register circuit	327/202	327/203	Takahashi; Hiroyuki
US 6483943 B1	USPAT	20021119	Feature value extraction methods and apparatus for image recognition and storage medium for storing image analysis program	382/199	382/203; 382/205; 382/266	Takahashi; Hiroyasu
US 6474691 B2	USPAT	20021105	Seat belt guide	280/801.1	280/801.2; 280/807; 297/483	Izume; Tomoharu et al.
US 6474134 B1	USPAT	20021105	Method for adjusting sensitivity of acceleration sensor	73/1.38	29/25.35; 29/595; 73/514.34	Takahashi; Hiroyuki et al.
US 6464696 B1	USPAT	20021015	Electrical surgical operating apparatus	606/34	606/37; 606/38	Oyama; Masahide et al.
US 6462782 B1	USPAT	20021008	Data extraction circuit used for reproduction of character data	348/465	348/461; 348/468; 725/137	Honda; Yoshitaka et al.
US 6456559 B1	USPAT	20020924	Semiconductor integrated circuit	365/230.06	365/189.11	Takahashi; Hiroyuki et al.
US 6456549 B1	USPAT	20020924	Sense amplifier circuit and semiconductor storage device	365/207	365/191; 365/202; 365/205; 365/206	Sudo; Naoaki et al.
US 6456548 B2	USPAT	20020924	Sense amplifier circuit and semiconductor storage device	365/207	365/191; 365/202; 365/205; 365/206; 365/63	Sudo; Naoaki et al.
US 6445215 B1	USPAT	20020903	Logic circuit with single charge pulling out transistor and semiconductor integrated circuit using the same	326/105	326/112	Takahashi; Hiroyuki et al.